

JEDEC STANDARD

Serial Flash Discoverable Parameters (SFDP)

JESD216E

(Revision of JESD216D.01, August 2019)

SEPTEMBER 2021

JEDEC SOLID STATE TECHNOLOGY ASSOCIATION



NOTICE

JEDEC standards and publications contain material that has been prepared, reviewed, and approved through the JEDEC Board of Directors level and subsequently reviewed and approved by the JEDEC legal counsel.

JEDEC standards and publications are designed to serve the public interest through eliminating misunderstandings between manufacturers and purchasers, facilitating interchangeability and improvement of products, and assisting the purchaser in selecting and obtaining with minimum delay the proper product for use by those other than JEDEC members, whether the standard is to be used either domestically or internationally.

JEDEC standards and publications are adopted without regard to whether or not their adoption may involve patents or articles, materials, or processes. By such action JEDEC does not assume any liability to any patent owner, nor does it assume any obligation whatever to parties adopting the JEDEC standards or publications.

The information included in JEDEC standards and publications represents a sound approach to product specification and application, principally from the solid state device manufacturer viewpoint. Within the JEDEC organization there are procedures whereby a JEDEC standard or publication may be further processed and ultimately become an ANSI standard.

No claims to be in conformance with this standard may be made unless all requirements stated in the standard are met.

Inquiries, comments, and suggestions relative to the content of this JEDEC standard or publication should be addressed to JEDEC at the address below, or refer to www.jedec.org under Standards and Documents for alternative contact information.

Published by
©JEDEC Solid State Technology Association 2021
3103 North 10th Street
Suite 240 South
Arlington, VA 22201-2108

JEDEC retains the copyright on this material. By downloading this file the individual agrees not to charge for or resell the resulting material.

PRICE: Contact JEDEC

Printed in the U.S.A.
All rights reserved

SERIAL FLASH DISCOVERABLE PARAMETERS (SFDP) STANDARD**Contents**

	Page
Foreword	viii
Introduction	viii
1 Scope	1
2 Normative reference	1
3 Terms and definitions	2
4 Read SFDP Command Protocol	3
4.1 Instruction	3
4.2 Address	3
4.3 Wait States	4
4.4 Clock Rate	4
4.5 Command Modes	4
4.5.1 Read SFDP (1-1-1) Mode	5
4.5.2 Read SFDP (2-2-2) Mode	6
4.5.3 Read SFDP (4S-4S-4S) Mode	6
4.5.4 Read SFDP (4S-4D-4D) Mode	7
4.5.5 Read SFDP (8D-8D-8D) Mode	7
4.5.6 Fetch SFDP (1-1-1) Mode	8
4.5.7 Fetch SFDP (2-2-2) Mode	8
4.5.8 Fetch SFDP (4S-4S-4S and 4S-4D-4D) Mode	9
4.5.9 Fetch SFDP (8D-8D-8D) Mode	9
5 Read SFDP Behavior	10
5.1 Security	10
5.2 Reset and Hold Functions	10
5.3 Read Wrap	10
5.4 SFDP Address Boundary Wrap	10
5.5 Reserved SFDP Locations	10
6 SFDP Database	11
6.1 SFDP Header Structure	11
6.2 SFDP Header	12
6.2.1 SFDP Header: 1 st DWORD	12
6.2.2 SFDP Header: 2 nd DWORD	13
6.2.3 Definition of SFDP Access Protocol Field	14
6.3 Parameter Headers	19
6.3.1 Parameter Header: 1 st DWORD	19
6.3.2 Parameter Header: 2 nd DWORD	19